

T16

- Supports Intel 80186 and 80188 Family
- Up to 1 Mbyte Zero WS Emulation Memory
- Transparent Genuine Real-Time Emulation
- Sophisticated Trace Recording Using HLL Lines
- Memory Protection of Target and/or Emulator
- 8 Trigger Inputs, 10 Trigger Outputs
- Hitex Concise Hardware Breakpoints
- Hardware Triggering: can be Sequenced
- Dual 2K by 56 Bits Filtered Trace Buffer
- Supports Intel OMF format

The T16 offers powerful debugging support for the Intel 80186/188 family including the 80C186/188EA/EB/EC. Each family member is supported with a pod change, by changing the emulation processor in the pod or with a mere software selection. The T16 operates in genuine real-time and does not require resources from the target system which facilitates effective and productive debugging sessions for the designer.

The HiTOP user interface operates under Windows* 3.1, Windows NT, and Windows 95. HiTOP is the same powerful user interface as used on all Hitex products. HiTOP provides unrestricted High-Level-Language (HLL) debugging capabilities. HLL debugging as well as assembly language debugging is supported with all symbols and comments displayed.

HiTOP is written and supported in-house by Hitex and features prompted menu selections with mouse clicks. The optional Performance Analysis feature provides increased efficiency and control in the optimization of the user code. The memory in the target system and/or the emulation memory can be access protected in blocks of 4 Kbytes.

The Hitex hardware breakpoint system stops the emulation before executing the instruction where the breakpoint is set. Jumps and returns are not taken preventing the user from losing debugging control. Execution breakpoints can be set in ROM as well as writable memory.

The complex trigger conditions can be used to control trace recording and time measurement. The triggers can be combined logically in sequences. The trace information is recorded in real-time and can be displayed in either high-level language or assembler form. The trace buffer size is a dual 2K by 56 bits and utilizes the exclusive Hitex Intelligent Lines Recording Method to efficiently use memory in a highly productive form for the user. A sophisticated filter mechanism is used to select only those areas of memory to be stored in the trace buffer.

PROCESSORS SUPPORTED:
80186, 80188, 80C186, 80C188,
80C186XL/80C188XL,
80C186EA/80C188EA,
80C186EB/80C188EB,
80C186EC/80C188EC.

AVAILABILITY:
Now

DEVELOPMENT PLATFORMS:
DOS, Windows 3.1, Windows 95
Windows NT

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